

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Paul A. Farrar	Examiner:	Nicole Blan
Serial No.:	10/681,481	Group Art Unit:	1792
Filed:	October 8, 2003	Docket:	1303.112US1
Customer No.:	73115	Confirmation No.:	7468
Title:	METHOD OF CLEANING SEMICONDUCTOR SURFACES		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the materials listed below are brought to the attention of the Examiner in connection with the above-identified patent application. Since a Notice of Allowance and Issue Fee Notification have been issued, Applicants understand that the materials listed below will be placed in the file and not considered by the U.S. Patent Office.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement.

Applicant brings to the attention of the Examiner the following Office Actions and responses associated with one or more of the inventors of the instant application and/or the assignee of the instant application. This material is available and accessible at the United States Patent and Trademark Office.

METHOD OF CLEANING SEMICONDUCTOR SURFACES

Application Serial No. 11/605,065 Reponse filed 6-25-09 to Non-Final Office Action mailed 3-25-09. 11 pgs
Application Serial No 11/605,065 Notice of Allowance Mailed 09/16/2009, 10

The Examiner is invited to contact the Applicant's Representative at the telephone number indicated if there are any questions regarding this communication.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 30th day of November, 2009.

LeShere Wolfe
Name


Signature